PTO/SB/08A

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number.:

Unassigned 10/777,261 Herewith

Filing Date:

First Named Inventor:

2/12/04

Art Unit:

Craig M. Darsow Unassigned 2825

**Examiner Name:** 

Unassigned Paul Dinh

Sheet 1 of 1

Attorney Docket Number.:

ROC920030338US1

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials			T²
PD	Patent Application entitled "Apparatus and Method for Performing Static Timing Analysian Integrated Circuit Design," Craig M. Darsow et al., filed February 12, 2004.		

Examiner Signature	מפ	Date Considered	00/15/0006
Examiner Signature	FD	Date Considered	08/15/2006

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup> Applicant's unique citation designation number (optional).
<sup>2</sup> Applicant is to place a check mark here if English language translation is attached.